

10 MCQ For IBPS IT Officer Exam 2017

1)The addressing mode/s, which uses the PC instead of a general purpose register is _____

- a)Indexed with offset
- b)Relative
- c)Definite
- d)Direct
- e)None of these

2)A low memory can be connected to microprocessor by using

- a)Ready
- b)New
- c)Hold
- d)Operand
- e)None of these

3)In case of, Zero-address instruction method the operands are stored in _____

- a)Offset
- b)Register
- c)Push down stack
- d)Indexed
- e)None of these

4)The addressing mode used in an instruction of the form ADD X, Y, is

- a)Absolute
- b)Logical
- c)Address
- d)Relative
- e)None of these

5)The bits used to indicate the status of the page in the memory is called _____

- a)Control bits
- b)Frame
- c)Memory
- d)Page
- e)None of these

6)Microprocessor 8085 is the enhanced version of with essentially the same construction set

- a)6800
- b)7000
- c)7500
- d)8080
- e)None of these

7)The _____ bit is used to indicate the validity of the page.

- a)Invalid bit
- b)Dirty bit
- c)Modify bit
- d)Valid bit
- e)None of these

8)The addressing mode used in the instruction PUSH B is

- a)Register
- b)Array
- c)Stack
- d)Direct
- e)None of these

9)The page table should be ideally situated within _____

- a)Cache
- b)Function
- c)MMU
- d)TLB
- e)None of these

10)If the page table is large then it is stored in _____

- a)Secondary storage

- b)Main memory
- c)Hard disk
- d)Floppy disk
- e)None of these

Answers:

- 1)b
- 2)a
- 3)c
- 4)a
- 5)a
- 6)d
- 7)d
- 8)a
- 9)c
- 10)b